

Figure 1

Eickemeyer et al.

AUS920000649US1

Mixed-Mode Hardware Multithreading

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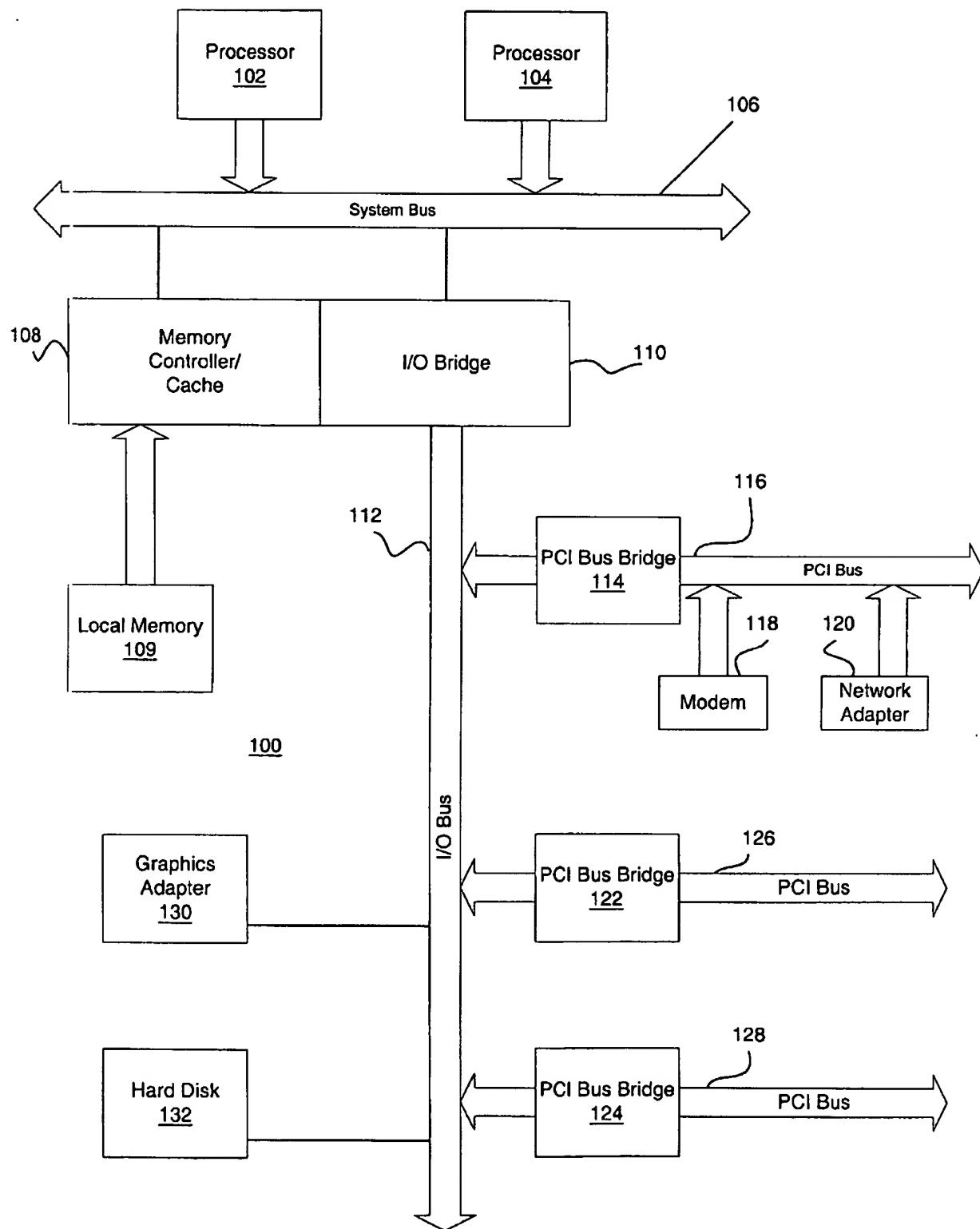


Figure 2

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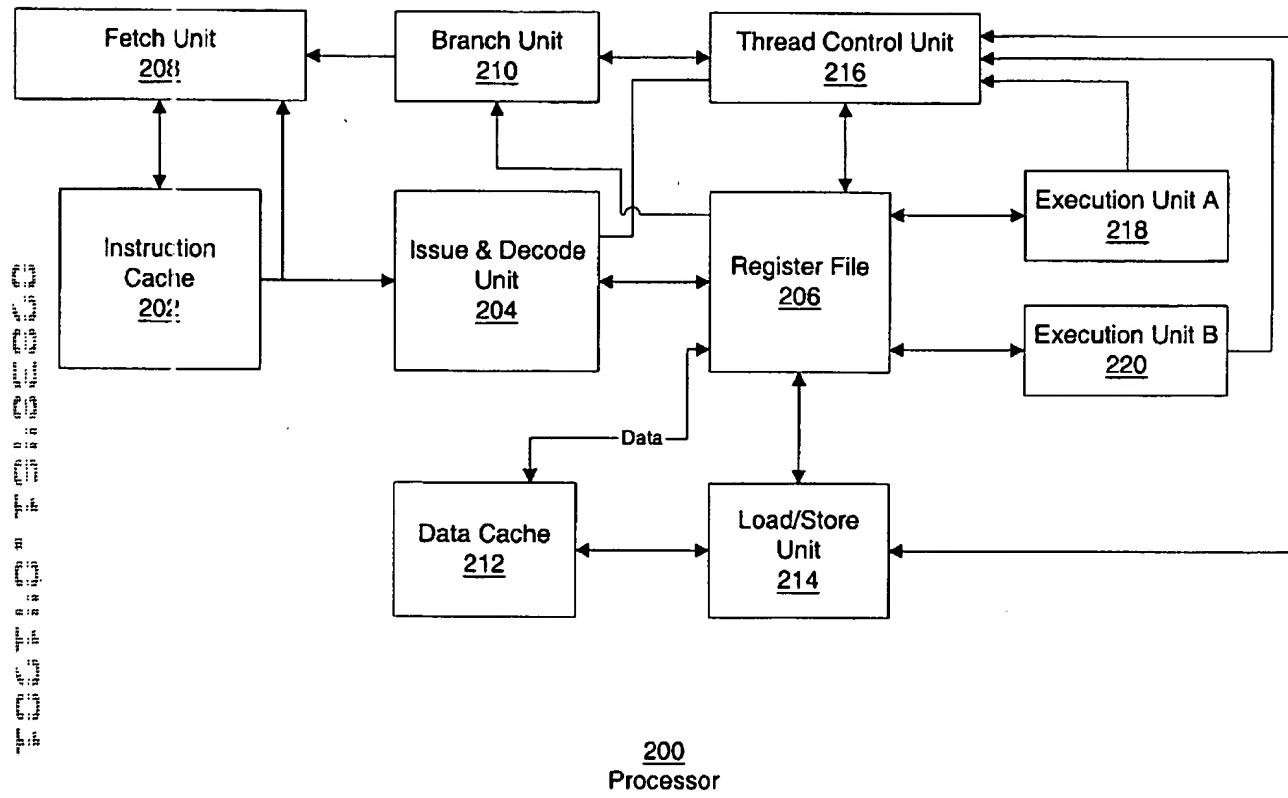


Figure 3

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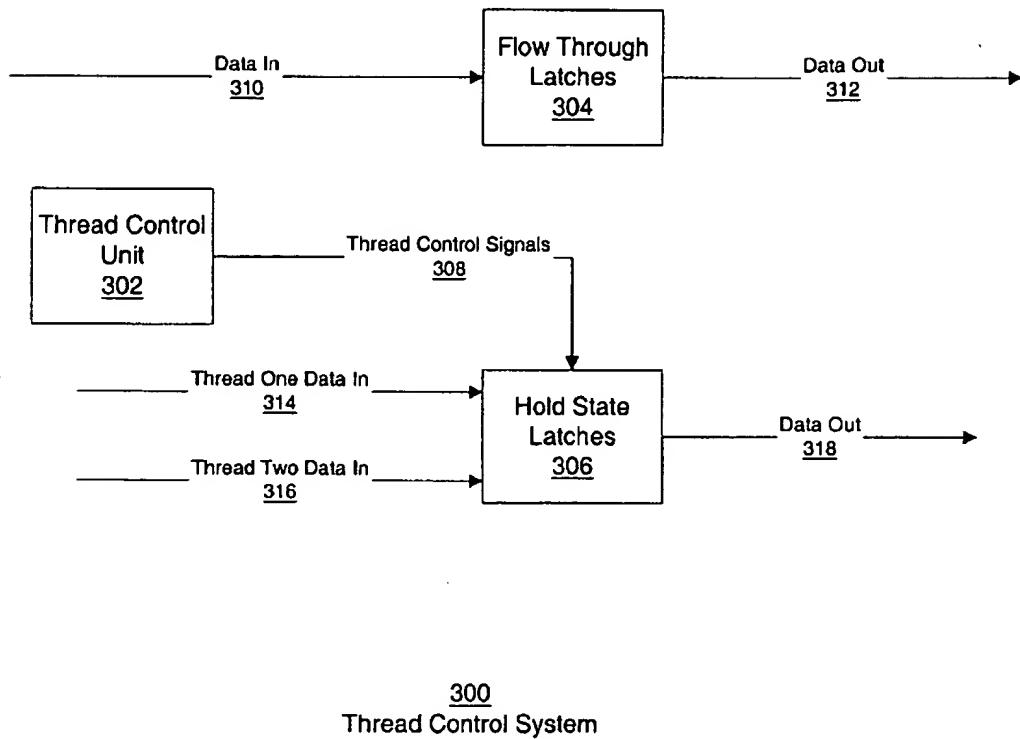


Figure 4

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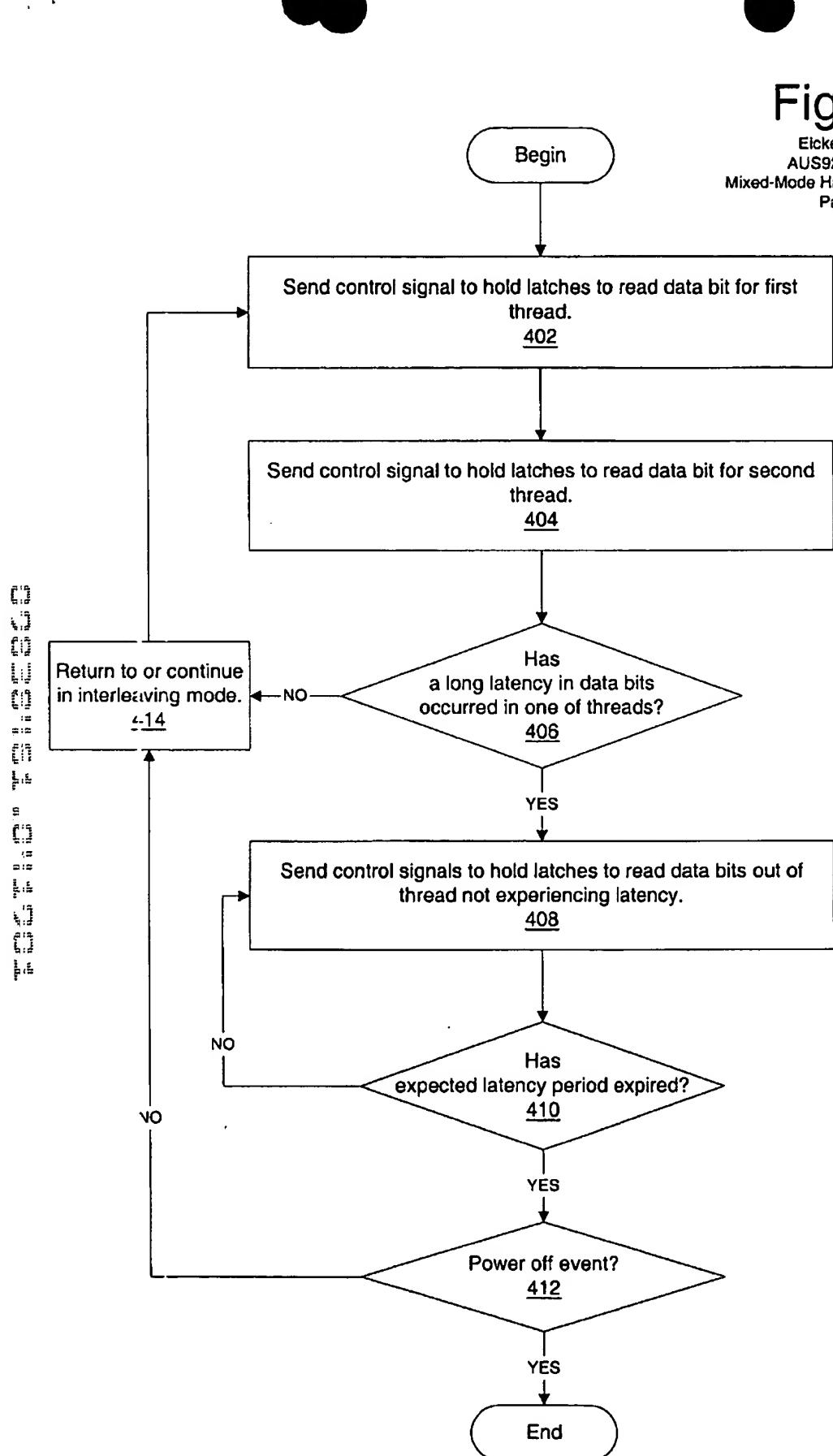


Figure 5

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